Fig.

LOGIC CIRCUIT FORMING AREA (LCFA)

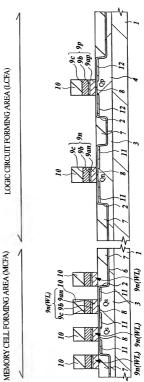


Fig. 2

LOGIC CIRCUIT FORMING AREA (LCFA) MEMORY CELL FORMING AREA (MCFA)

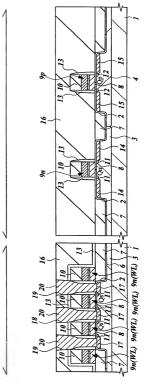


Fig. 3

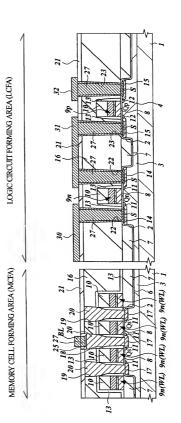


Fig. 4

LOGIC CIRCUIT FORMING AREA (LCFA) MEMORY CELL FORMING AREA (MCFA)

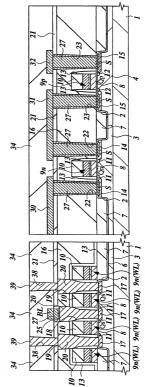
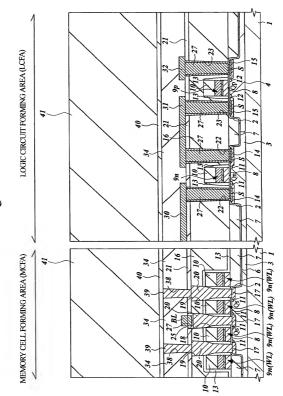
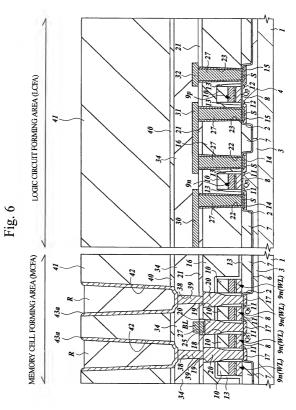


Fig. 5







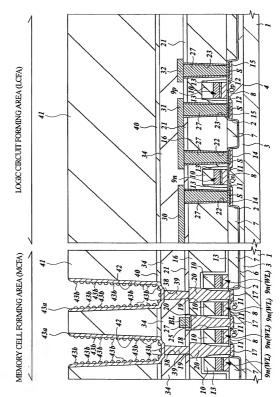


Fig. 8

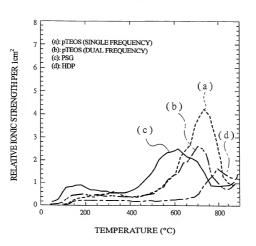
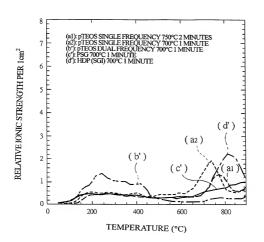
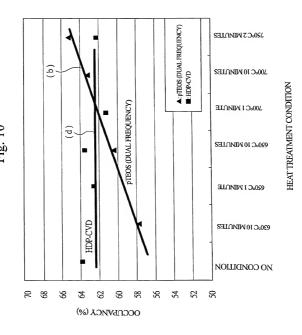
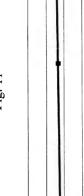
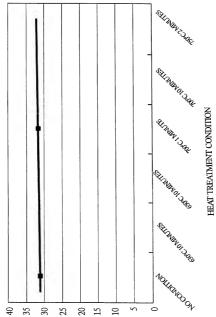


Fig. 9



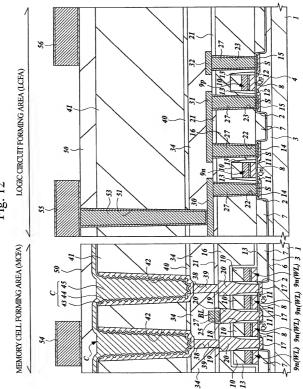






CAPACITANCE (fF)







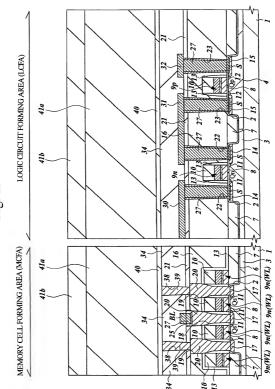
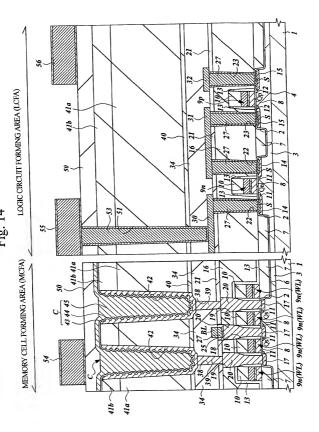


Fig. 14



TOTAL STREET, STREET,

Fig. 15

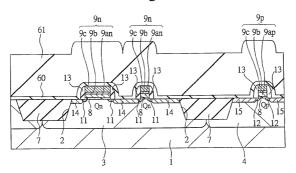


Fig. 16

